THE EUPILOT

Pilot using Independent Local & Open Technologies

The European PILOT will build and deploy a RISC-V based pre-exascale accelerator demonstrator

The first end-to-end European open source and open standards based software and hardware system, including two accelerator chips (HPC or ML/AI), and innovative power and liquid cooling datacenter deployment solutions.

Objectives

The European PILOT project's overall goal is to demonstrate European technology by achieving the following objectives:

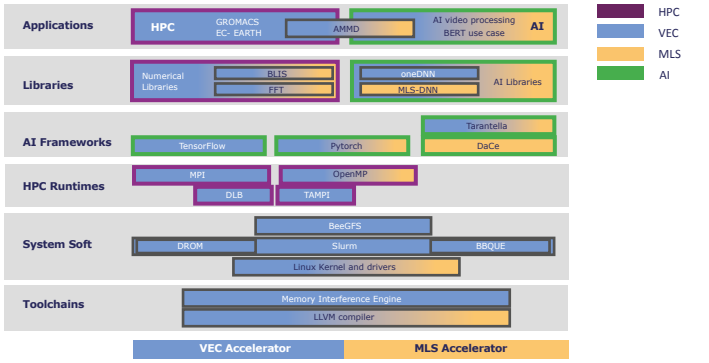
- Extend open source to include open-source hardware for HPC
- Software/Hardware co-design for improved application performance and system energy efficiency
- HPC and HPDA applications

- Stimulate European collaboration
- System integration innovations
- Combine industry-standard methodology and cutting-edge research to accelerate exploitation

Approach

The European PILOT contains three streams to deliver an European open source and open standards-based software and hardware integrated system.

Software

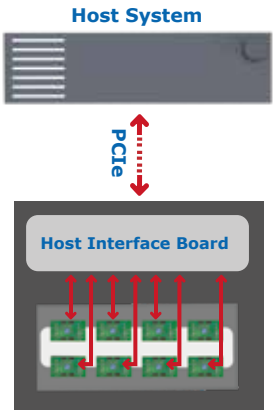


Hardware

Drivers ⋯→ **OS** ⋯→ **Compilers** ⋯→ **Frameworks** ⋯→ **Apps**

Chips ⋯→ **Modules** ⋯→ **Boards**

The European PILOT Chiplets Module



EUPILOT Accelerator System

Boards ⋯→ **Modules** ⋯→ **Systems** ⋯→ **Liquid Immersion Racks**

Partners

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www.eupilot.eu

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