EUPILOT: a RISC-V pre-exascale HPC and AI accelerator demonstrator

OBJECTIVES

- Extend open source to include open source hardware for HPC
- Co-design Software/Hardware for improved application performance and overall energy efficiency
- HPC, AI, ML and HPDA applications

- Stimulate European collaboration
- Innovative System integration
- Combine industry-standard methodology and cutting-edge research to accelerate exploitation

Co-funded by the European Union

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@EUPilot: Pilot using Independent Local & Open Technologies

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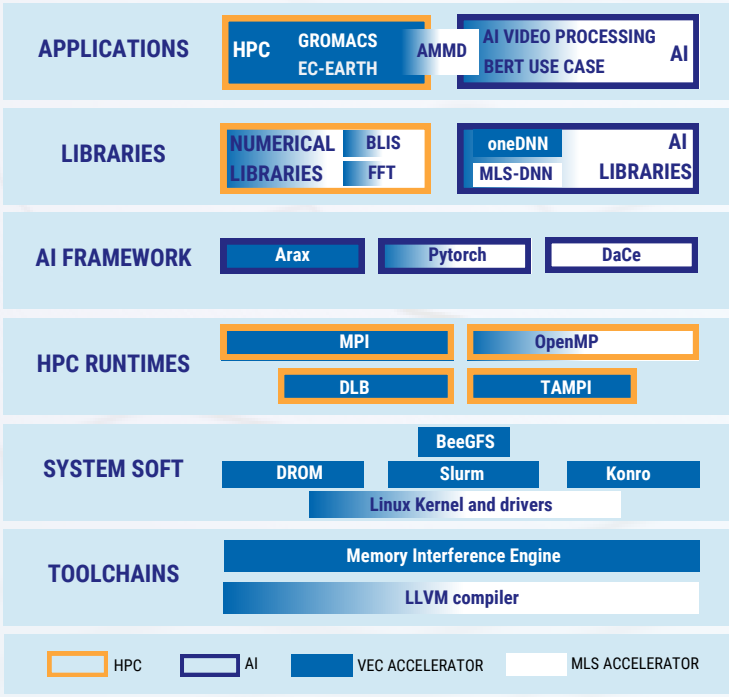
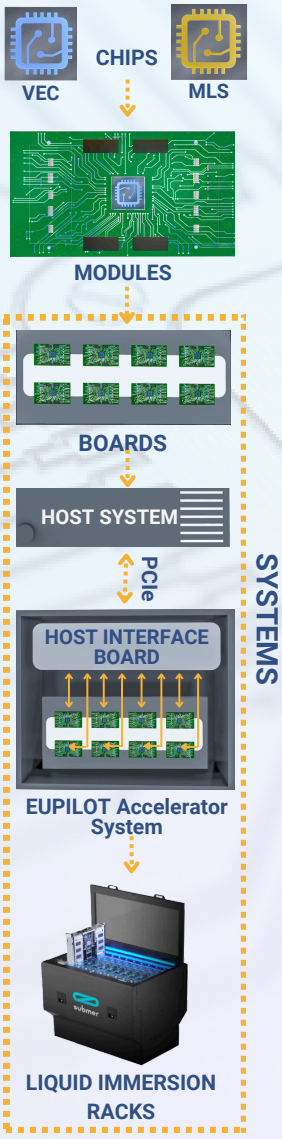
The first end-to-end European open source and open standards based software and hardware accelerator system, including two chips (HPC and ML/AI), and innovative power and liquid immersion cooling deployment solutions.

SOFTWARE



HARDWARE



EUPILOT: From Chips to Deployment

HW accelerator efforts built upon key IPs from EPI project

Improved tape-outs in 12nm (from 22nm in EPI), with increased compute and speed

Target HPC (VEC) and AI/ML (MLS) applications

Deploy OCP based systems to data center with liquid immersion cooling tanks